

REMARKS

Entry of this amendment prior to examination is respectfully requested.

By the present amendment, the continuing data information required by 37 CFR § 1.72 is provided. In addition, the original claims 1-22 have been cancelled, without prejudice, and new claims 23-27 have been added. These claims particularly define the combination of a central processing unit, a flash memory array, including a first region to be stored with a program for the central processing unit and a second region to be stored with information, each of the first and second regions including electrically erasable and programmable non-volatile memory elements, each having a layer to store electrons. In addition, the claimed microprocessor includes a volatile memory circuit capable of storing the information in a peripheral circuit coupled to the volatile circuit. Still further, the claimed microprocessor sets forth that information is transferred from the second region to the volatile memory circuit in response to an initialization operation of the microprocessor, after which the peripheral circuit is controlled by the information stored in the volatile memory. As such, it is respectfully submitted that newly presented claims 23-27 patentably define over the cited prior art, and also define a separately patentable invention from the inventions claimed in the parent patents. Accordingly, entry of newly submitted claims 23-27 and allowance thereof is earnestly solicited.

In view of the foregoing, entry of the present amendments and examination of the above-identified application on the merits in due course, are respectfully requested.

Kindly charge any additional fees due, or credit overpayment of fees, to
Deposit Account No. 01-2135 (501.35917CC5).

Respectfully submitted,

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By



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